

being an integer) continuous lines of the solid-state image sensor for still picture recording or dynamic image processing.

15. (Amended) An electronic imaging system comprising:

a solid-state image sensor having a two-dimensional array of pixels capable of converting light incident thereon to electric signal, the pixels being arranged in a plurality of horizontal lines, the lines being arranged vertically one under another;

a color filter arranged on an incident plane of the solid-state image sensor having a line sequential primary color mosaic pattern; and

control means for selectively controlling a mode for sequential scan reading out pixel signals concerning the whole pixels of the solid-state image sensor for still picture recording, a mode for reading out pixel signal sums by utilizing a plurality of vertical registers each of n ($n \geq 2$, n being an integer) lines among m ($m \geq 3$, m being an integer) lines of the solid-state image sensor for still picture recording or dynamic image processing, and a mode for reading out pixel signal sums by utilizing a plurality of vertical registers of n lines among m lines in k ($k \geq 6$, k being an integer) partially continuous lines of the solid-state image sensor for still picture recording or dynamic image processing.

38. (Amended) An electronic imaging system comprising:

a solid-state image sensor having a two-dimensional array of pixels capable of converting light incident thereon to electrical signals, the pixels being arranged in a plurality of horizontal lines, the lines being arranged vertically one under another;

a color filter arranged on an incident plane of the solid-state image sensor having a line sequential primary color mosaic pattern; and

control means for selectively controlling a mode for sequential scan reading out pixel signal concerning the whole pixels of the solid-state image sensor for still picture recording and a mode for reading out pixel signal sums of n lines out of every m lines within partially continuous k lines of the solid-state image sensor by utilizing a plurality of vertical registers for still picture recording or dynamic image processing, wherein

$n \geq 2$, n being an even number,

$m \geq n+1$, m being an odd number,

$k \geq 2m$, k being an integer.

Kindly add new claims 39 and 40 as follows:

39. (New) A controller for reading out color image signal from a destructive read-out type imager with a primary color Bayer filter, said controller comprising:

a mode selector unit configured to select one of plural read-out modes according to usage of said color image signal read from the imager, said read-out modes including:

(a) a mode for reading out gamut of the imager for use with recording a still picture;

(b) a mode for reading out with eliminating every third line and summing every line pair to be read-out within the gamut of the imager for use with controlling AE and AWB function; and

(c) a mode for reading out with eliminating every third line and summing every line pair to be read-out within partial imaging area of the imager for use with controlling AF function.

40. (New) A method of reading out color image signal from a destructive read-out type imager with a primary color Bayer filter, comprising the steps of:

selecting one of plural read-out modes according to usage of said color image signal read out from the imager, said read-out modes including:

(a) a mode for reading out gamut of the imager for use with recording a still picture;

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(b) a mode for reading out with eliminating every third line and summing every line pair to be read-out within gamut of the imager for use with controlling AE and AWB function; and

(c) a mode for reading out with eliminating every third line and summing every line pair to be read-out within partial imaging area of the imager for use with controlling AF function.
